



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 4104

SERIAL NUMBER 10/765,917	FILING OR 371(c) DATE 01/29/2004 RULE	CLASS 029	GROUP ART UNIT 3729	ATTORNEY DOCKET NO. 501.43386X00
-----------------------------	--	--------------	------------------------	--

APPLICANTS

Yuji Wada, Tachikawa, JAPAN;
 Susumu Kasukabe, Yokohama, JAPAN;
 Takehiko Hasebe, Yokohama, JAPAN;
 Yasunori Narizuka, Hiratsuka, JAPAN;
 Akira Yabushita, Yokohama, JAPAN;
 Terutaka Mori, Urayasu, JAPAN;
 Akio Hasebe, Kodaira, JAPAN;
 Yasuhiro Motoyama, Kodaira, JAPAN;
 Teruo Shoji, Mitaka, JAPAN;
 Masakazu Sueyoshi, Kodaira, JAPAN;

** CONTINUING DATA *****

None PK

** FOREIGN APPLICATIONS *****

JAPAN 2003-075429 03/19/2003

OK PK

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/12/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	23	9	1
Verified and Acknowledged	<u>PL</u>	Examiner's Signature	Initials		

ADDRESS

20457

TITLE

Fabrication method of semiconductor integrated circuit device

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
----------------------------	---	---